

View all Publications for 2012/2013		
Books	Journals	Conferences
Computer Systems Design		
W. Ibrahim, and V. Beiu: On Enabling Redundant Designs for Nano Computations In V. Beiu, and U. Rückert (Eds.): Emerging Brain-Inspired Nano-Architectures, 2013 V. Beiu, J.M. Quintana, and M.J. Avedillo Threshold Logic Design and Implementations: From the Early Days into the Nanoera In V. Beiu, and U. Rückert (Eds.): Emerging Brain-Inspired Nano-Architectures, 2013 M.H. Sulieman, and V. Beiu From Single Electron Technology (SET) Full Adders to Optimal Practical SET Adders In V. Beiu, and U. Rückert (Eds.): Emerging Brain-Inspired Nano-Architectures, 2013 V. Beiu, and U. Rückert: Roadmap for Nano Architectures In V. Beiu, and U. Rückert (Eds.): Emerging Brain-Inspired Nano-Architectures, 2013 J. Nyathi, and V. Beiu: Advanced Techniques for Reducing Power Consumption In V. Beiu, and U. Rückert (Eds.): Emerging Brain-Inspired Nano-Architectures, 2013 V. Beiu, L. Zhang, A. Beg, W. Ibrahim, and M. Tache: Axon-Inspired Communication Systems In J.E. Morris and K. Iniewski (Eds.): Nanoelectronic Device Applications Handbook		

CRC Press (Taylor & Francis Group), USA, June/July 2013, In press

A. Beg, M.H. Sulieman, V. Beiu, and W. Ibrahim: Low-Power Reliable Nano Adders
In J.E. Morris and K. Iniewski (Eds.): Nanoelectronic Device Applications Handbook
CRC Press (Taylor & Francis Group), USA, June/July 2013, In press

G. Wendin, D. Vuillaume, M. Calame, S. Yitzchaik, C. Gamrat, G. Cuniberti, and V. Beiu SYMONE Project: SYNaptic MOlecular NETworks for Bio-inspired Information Processing International Journal of Unconventional Computing, vol. 8, no. 4
Nov. 2012 (IF ~ 0.648), 325-332

W. Ibrahim, V. Beiu, and A. Beg
On Optimum Reliability Sizing for CMOS Gates
IEEE Transactions on Reliability, vol. 61, no. 3
Sept. 2012 (IF ~ 1.288), 675-686

W. Ibrahim, V. Beiu, and A. Beg
GREDA: A Fast and More Accurate CMOS Gates Reliability EDA Tool
IEEE Transactions on Computer-Aided Design of Integrated Circuits and System, vol. 31, no. 4
Apr. 2012 (IF ~ 1.258), 509-521

F. Kharbash, V. Beiu, M. Tache, and W. Ibrahim
How Much Could Biasing Help Maximizing the Static Noise Margins
IEEE GCC Conference
Doha, Qatar, Nov. 17-20, 2013. In preparation

N. Acharya, J. L. Raju, M. Tache, A. Kumar, and V. Beiu

On Monte Carlo for Predictive Technology Models

IEEE GCC Conference

Doha, Qatar, Nov. 17-20, 2013, In preparation

W. Ibrahim, V Beiu, M. Tache, and F. Kharbash

On Schmitt Trigger's and Other Inverters

European Workshop on CMOS Variability

VARI'13/PATMOS'13

Karlsruhe, Germany, Sept. 9-11, 2013, Submitted

M. Tache, V Beiu, W. Ibrahim, F. Kharbash, and M. Alioto

Sizing for Static Noise Margins Revisited

European Workshop on CMOS Variability

VARI'13/PATMOS'13

Karlsruhe, Germany, Sept. 9-11, 2013, Submitted

V Beiu, A. Beg, W. Ibrahim, F. Kharbash, and M. Alioto

Enabling Sizing for Enhancing the Static Noise Margins

IEEE International Symposium on Quality in Electronic Design ISQED'13

Santa Clara, CA, USA, Mar. 4-6, 2013, In press

P. Santagati, and V Beiu: Will Thermal Noise Affect Nano-Communications?

IEEE International Conference on Communications, Signal Processing,

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